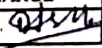

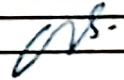


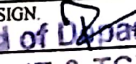

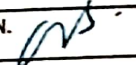
 <b>JCEI'S</b> <b>JAIHIND COLLEGE OF ENGINEERING, KURAN</b> Affiliated to Savitribai Phule Pune University, Pune		TIME TABLE (ACADEMIC YEAR -2024-25)					
		DEPARTMENT:- Electronics and Telecommunication Engineering					
CLASS :- T.E.		DIV:	SEMESTER: I		W.E.F. :- DATE: 01/07/2024		
NAME OF CLASS TEACHER:- Prof. Bhor A.S.					BLOCK / CLASS NO:- 14		
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	MC	MC	EFT	EFT	EFT	DBM	
10:15-11:15	DC	JAVA	DBM	DBM	MC	DC	
11:15-12:00	RECESS						
12.00-01:00	DBM-T1 MAC 5 - T2 SD-T3	MAC 5 - T1 DBM-T2 DC T3	SD-T1 MC - T2 JAVA - T3	JAVA-T1 DC-T2 DBM - T3	MC-T1 JAVA-T2 MAC 5 - T3	NPTEL	
01.00-02:00							
02.00-02:15	RECESS						
02.15-03:15	JAVA	DC	JAVA	LIBRARY	DC-T1 SD- T2 MC - T3	NPTEL	
03.15-04:15	LIBRARY	EFT- TUT(T1/T2/T3)	LIBRARY				
Batches :- T1 :- 1-25      T2- 26-50      T3-51-70							
Name Of Subject	Abbreviation	Subject Code	CLASS/LAB NO.		Name of Subject Teacher	Guardian for Roll Nos.	
Digital Communication (TH)	DC	304181	S014		Prof. Chaugule S. D.		
Electromagnetics field theory (TH)	EFT	304182	S014		Dr. Mulajkar R. M.		
Database Managemet (TH)	DBM	304183	S014		Prof. Punde P.S.		
Microcontrollers (TH)	MC	304184	S014		Prof. Bhor A.S.		
EL-I (FUNDAMENTAL OF JAVA) (TH)	JAVA	304185	S014		Prof. Shinde S.E.		
Digital Communication Lab (PR)	DC Lab	304186	S002		Prof. Chaugule S. D.		
Database Managemet Lab (OR)	DBM Lab	304187	S024		Prof. Punde P.S.		
Microcontrollers Lab (PR)	MC Lab	304188	S024		Prof. Bhor A.S.		
EL-I (JAVA) Lab (OR)	Java Lab	304189	S020		Prof. Shinde S.E.		
Skill Development (TW)	SD Lab	304190	S007		Prof. Raut P.K.		
Mandatory Audit Course 5 (TW)	MAC-5	304191A	-		Prof. Raut P.K.		
	REVISION	PREPARED BY	VERIFIED BY	VERIFIED BY	APPROVED BY	PAGE No.	CHAPTER No.
W.E.F	No. : 00	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC	DSGN.: PRINCIPAL	01 of 01	LIST-JCOE-01
DATE: 01/07/2024	DATE: 01/07/2024	SIGN. 	SIGN. 	SIGN.	SIGN. 		
F/JCOE/AC/CTT-10/00/01.01.2024							

Head of Department  
(E & TC)  
JCEI's Jaihind College of Engineering,  
Kuran, Tal-Junnar, Dist. Pune

		<b>JCEI'S</b> <b>JAIHIND COLLEGE OF ENGINEERING, KURAN</b> Affiliated to Savitribai Phule Pune University, Pune				<b>TIME TABLE</b> (ACADEMIC YEAR - 2024-25)	
		<b>DEPARTMENT:- Electronics and Telecommunication Engineering</b>					
CLASS :- B.E.		DIV:		SEMESTER:-I		W.E.F. :- DATE:01/07/2024	
NAME OF CLASS TEACHER:- Prof.Chaugule Swapnali.D.						BLOCK / CLASS NO:- 5 023	
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	CC	VLSI	EPD	VLSI	EPD	MAC 7	
10:15-11:15	EPD	CC	IOT	RMT	IOT		
11:15-12:00	RECESS						
12.00-01:00	IOT-B1	VLSI-	RMT	RMT	NPTEL	Library	PP-I B1/B2
01.00-02:00	B2		IOT	CC		VLSI	
02.00-02:15	RECESS						
02.15-03:15	RMT-B1	CC	PP-I	B1/B2	CC-B1 RMT-B2	VLSI-B1 B2	IOT- PP-I B1/B2
03.15-04:15	B2						NPTEL
Batches :- B1 :-01 To 12    B2-13 to 24							
Name Of Subject	Abbreviation	Subject Code	CLASS/LAB NO.		Name of Subject Teacher	Guardian for Roll Nos.	
Radiation & Microwave Techniques (TH)	RMT	404181	S023		Dr. Mrs.Dhede V.M		
VLSI Design & Technique (TH)	VLSI	404182	S023		Prof. Chaugule S.D.		
Cloud computing (TH)	CC	404183	S023		Prof. Chaugule Sw.D.		
Modernized IOT (TH)	IOT (EL-III)	404184	S023		Prof. Chaugule Sw.D.		
Electronic Product Design (TH)	EPD(EL-IV)	404185	S023		Dr. Mrs.Dhede V.M		
Lab Practice-I (RMT+CC) (OR=TW)	LP-I	404186	S001+S022		Dr. Mrs.Dhede V.M/ Prof. Chaugule Sw.D.		
Lab Practice-II (VLSI+IOT(EL-III) (PR+TW)	LP-II	404187	S020+S024		Prof. Prof. Chaugule S.D./ Prof. Chaugule Sw. D.		
Project Phase -I (OR)	PP-I	404188	S007		Dr. Malaykar R. M.		
Audit Course 7 (TW)	AC	404189			Prof Raut P.K.		
WEF	REVISION	PREPARED BY	VERIFIED BY	VERIFIED BY	APPROVED BY	PAGE No.	CHAPTER No.
-	No. : 00	DSGN: T T INCHARGE	DSGN.: HOD	DSGN.:IQAC	DSGN.: PRINCIPAL	01 of 01	LIST-JCOE-01
DATE:01/07/2024	DATE:01/07/2024	SIGN. 	SIGN. 	SIGN. 	SIGN. 		

F/JCOE/AC/CTT-10/00/01.01.2024

**JCEI's Jaihind College of Engineering.**  
 Kuran, Tal-Junnar, Dist. Pune  
 (E & TC)





JCEI'S  
JAIHIND COLLEGE OF ENGINEERING, KURAN  
Affiliated to Savitribai Phule Pune University, Pune

TIME TABLE  
(ACADEMIC YEAR -2024-25)

DEPARTMENT:- Electronics & Telecommunication Engg

CLASS :- S.E.		DIV:		SEMESTER: I		W.E.F. :- DATE: 01/07/2024	
NAME OF CLASS TEACHER:- Prof. Bhingardive A.A.						BLOCK / CLASS NO:- 13	
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	DS	DS	M-III	DC	DS	ELC	
10:15-11:15	EXC	EXC	EXC	EXC	DC	M-III	
11:15-12:00	RECESS						
12.00-01:00	DC	DC	DS	M-III	M-III TUT	DS S1 EXC S2 ESD S3	
01.00-02:00	M-III	ELC	ELC	ELC	Library		
02.00-02:15	RECESS						
02.15-03:15	ESD S1	EXC S1	Library S1	DC -S1	ELC S1	MAC 3	
03.15-04:15	DS S2 Library- S3	ESD S2 DC- S3	ELC S2 DS S3	Library S2 ELC S3	DC -S2 EXC S3		
Batches :- S1 :- 1-25 S2- 26-50 S3- 51 to onwards							
Name Of Subject	Abbreviation	Subject Code	CLASS/LAB NO.	Name of Subject Teacher	Guardian for Roll Nos.		
Engineering Maths-III (TH/TW)	MIII	207005	S013	Prof. Hande K.V.			
Electronic Circuits (TH)	EXC	204181	S013	Dr. Muljkar R.M.			
Digital Circuits (TH)	DC	204182	S013	Prof. Bhingardive A.A.			
Electrical Cicuits (TH)	ELC	204183	S013	Prof. Rahane S. L.			
Data Structure (TH)	DS	204184	S013	Prof. Punde P.S.			
Electronic Circuits Lab (PR)	EXC Lab	204185	S002	Prof. Chaugule Sw. D.			
Digital Circuits Lab (PR)	DC Lab	204186	S001	Prof. Bhingardive A.A.			
Electrical Circuits Lab (TW)	ELC Lab	204187	G001	Prof. Rahane S. L.			
Data Structure Lab (OR)	DS Lab	204188	S024	Prof. Punde P.S.			
Electronic Skill Development (TW)	ESD	204189	S007	Prof. Raut P. K.			
Mandatory Audit Course 3	MAC-3	204190	S007	Prof. Raut P. K.			
REVISION	PREPARED BY	VERIFIED BY	VERIFIED BY	APPROVED BY	PAGE No.	CHAPTER No.	
W.E.F	No : 00	DSGN : T T INCHARGE	DSGN : HOD	DSGN : IQAC	DSGN : PRINCIPAL	01 of 01	LIST-JCOE-01
DATE: 01/07/2024	DATE: 01/07/2024	SIGN.	SIGN.	SIGN.	SIGN.		

F/JCOE/AC/CTT-10/00/01.01.2024

Head of Department

(E & TC)

JCEI's Jaihind College of Engineering,  
Kuran, Tal-Junnar, Dist. Pune

